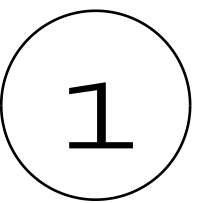
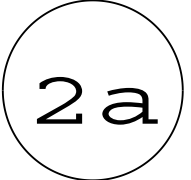
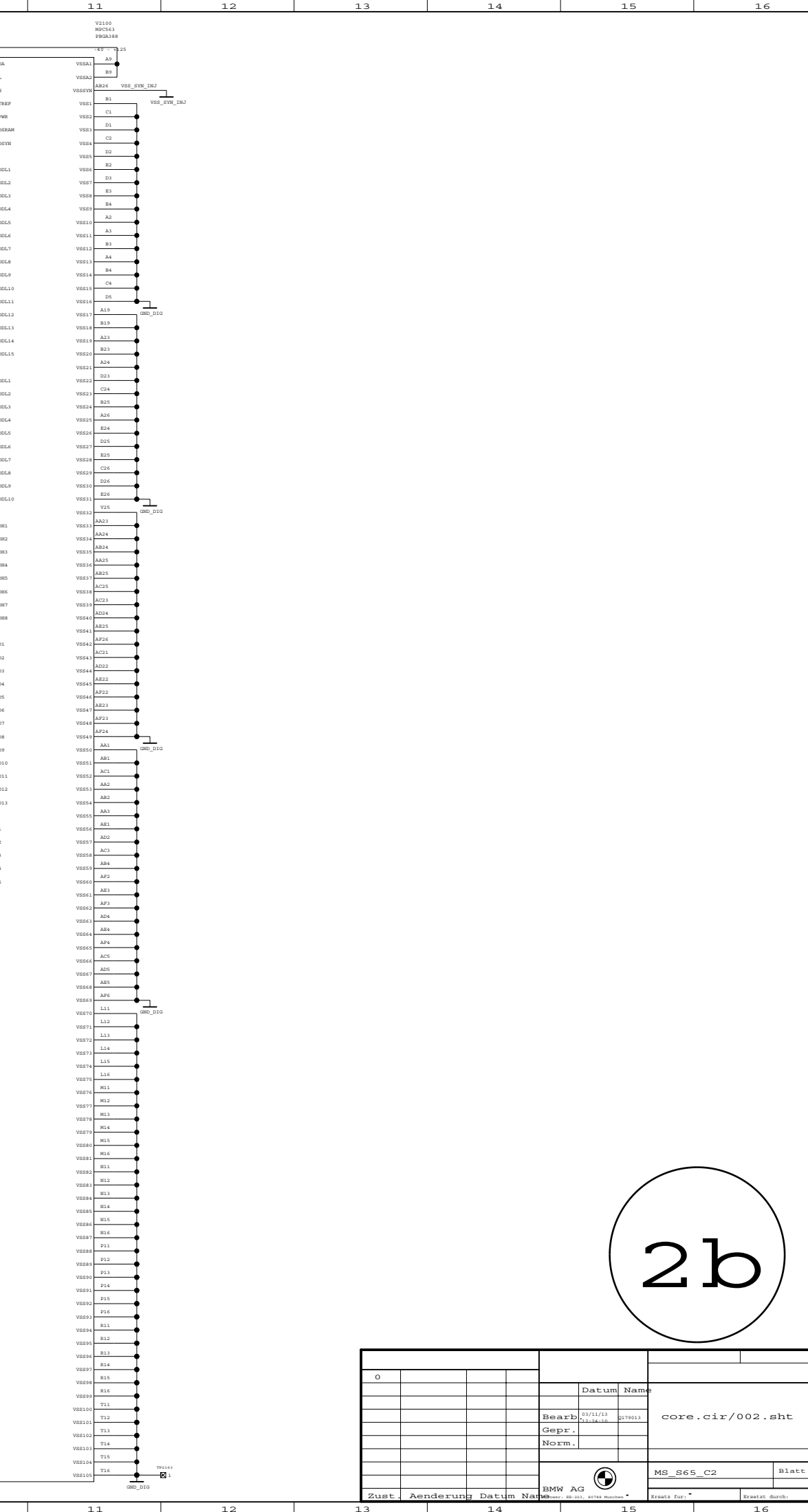
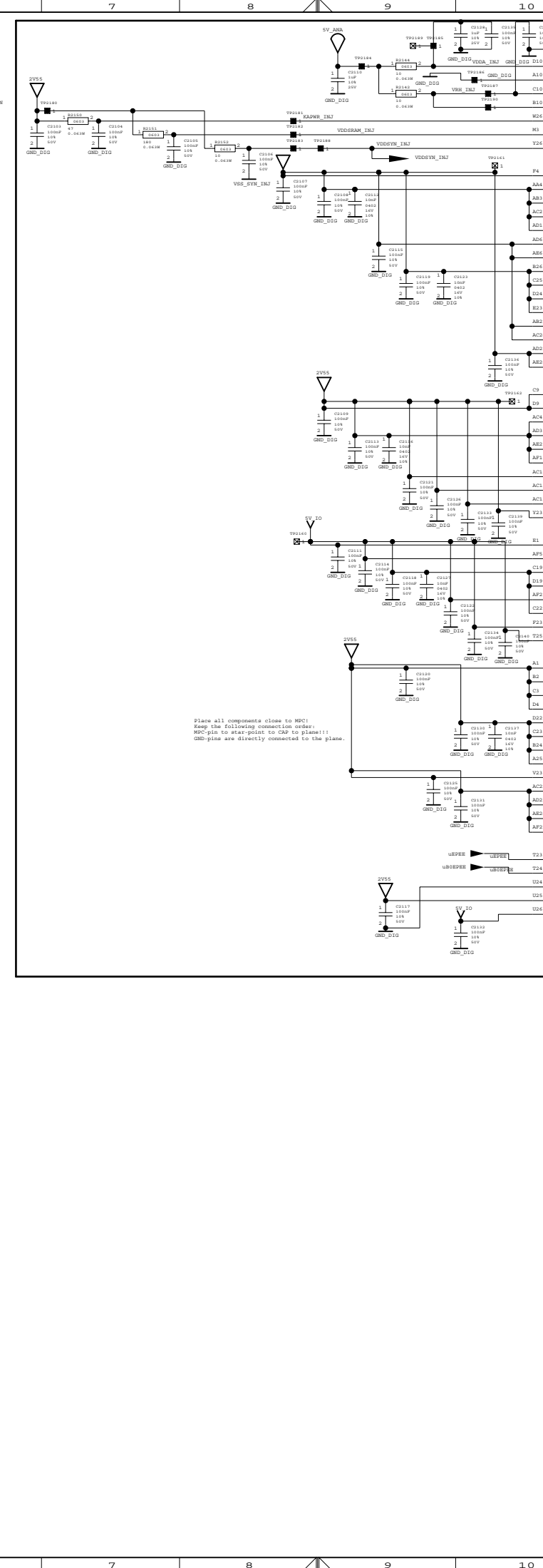
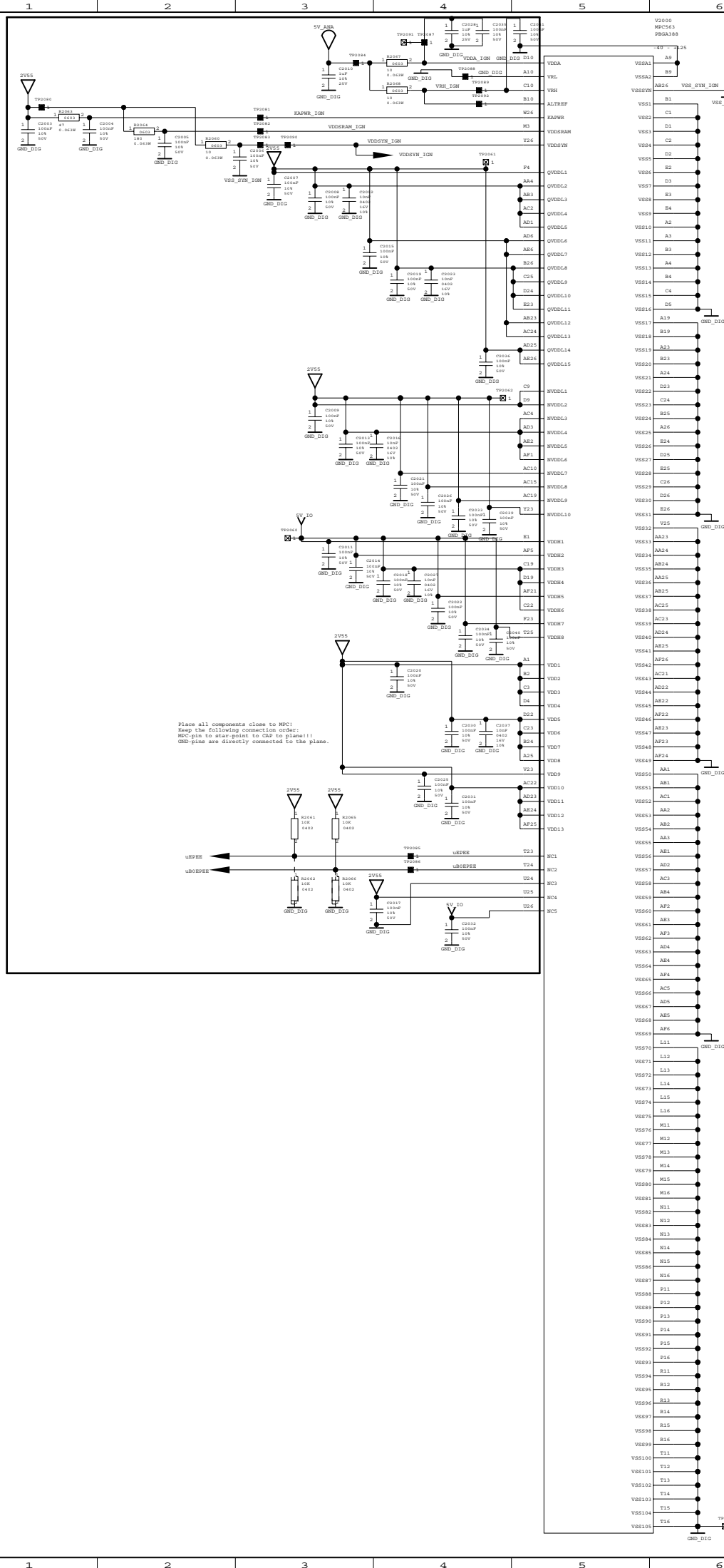


[illegible]

[illegible]

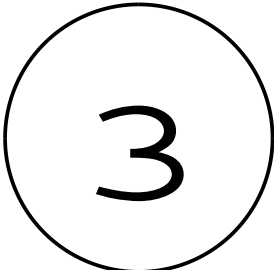


D		1. Name of the person or organization 2. Address 3. City 4. State 5. Zip		Datum Name			
				Report Date Gepr. Worm		CORE.CIR/001.sht MS-855-C2	
Zust.		Datum		 BMW AG MÜNCHEN		Blatt 1 81	
21	22	23	24				

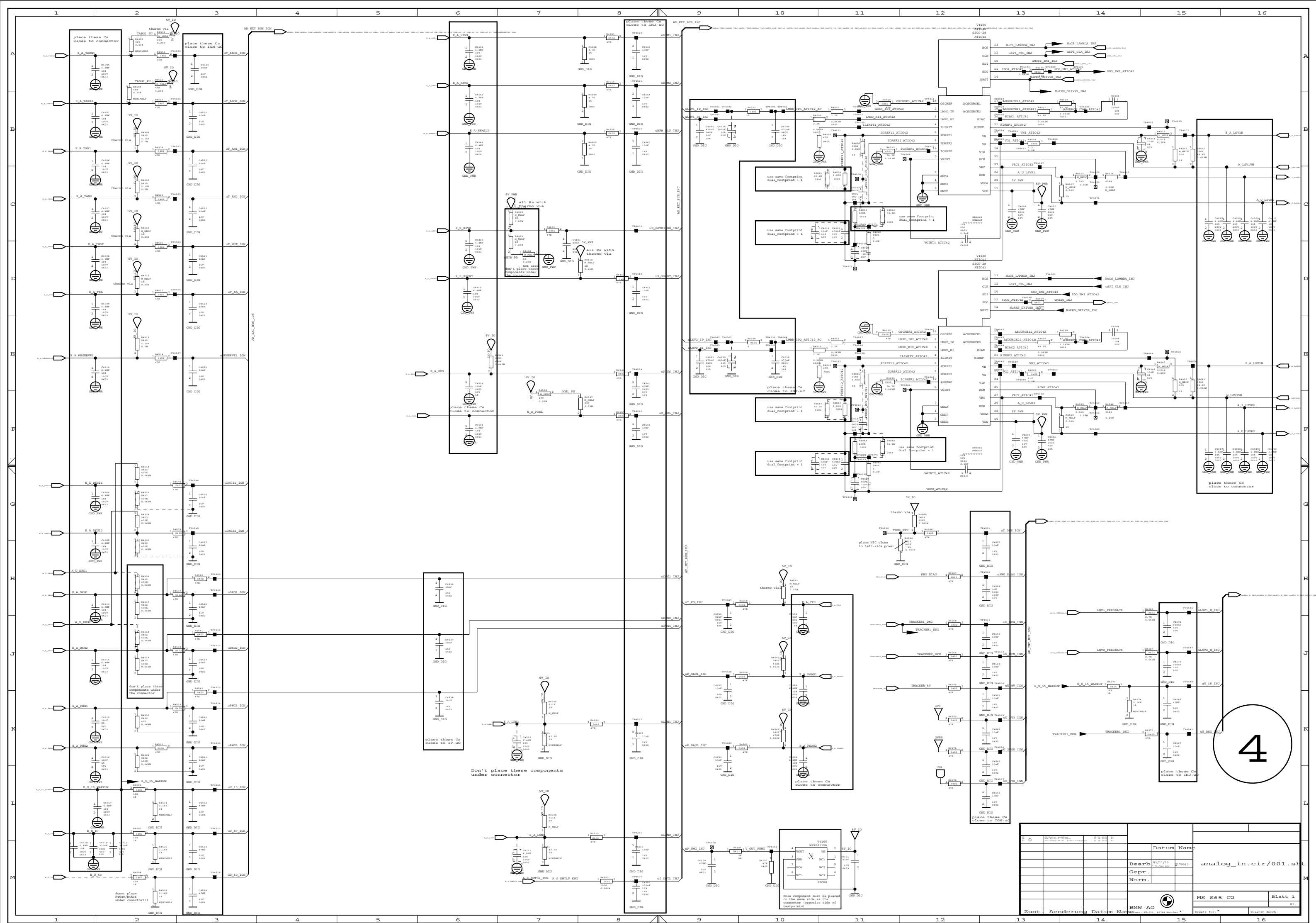


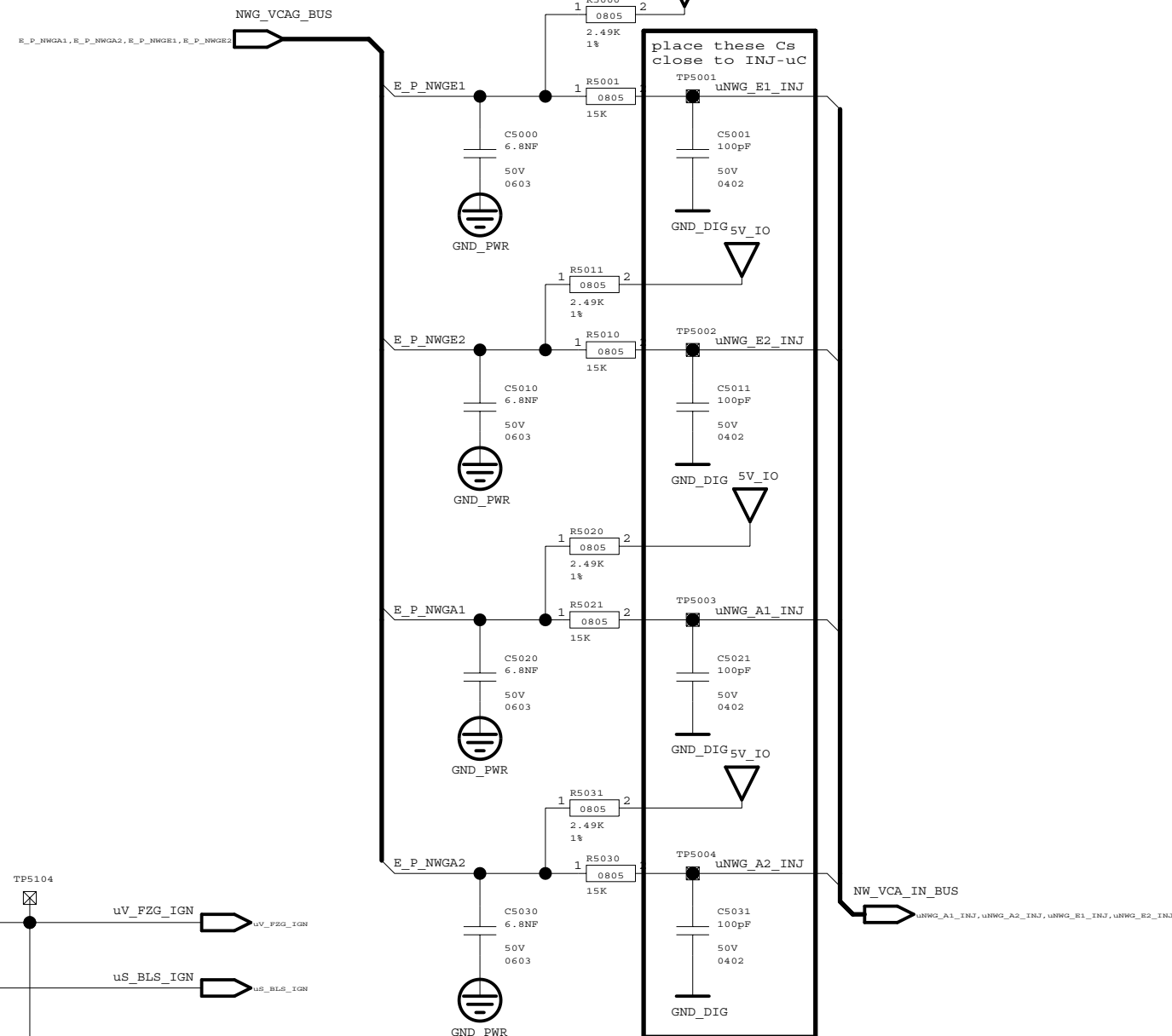
2b

0									
					Datum	Name			
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				Gepr.	10.04.08				
				Norm.					



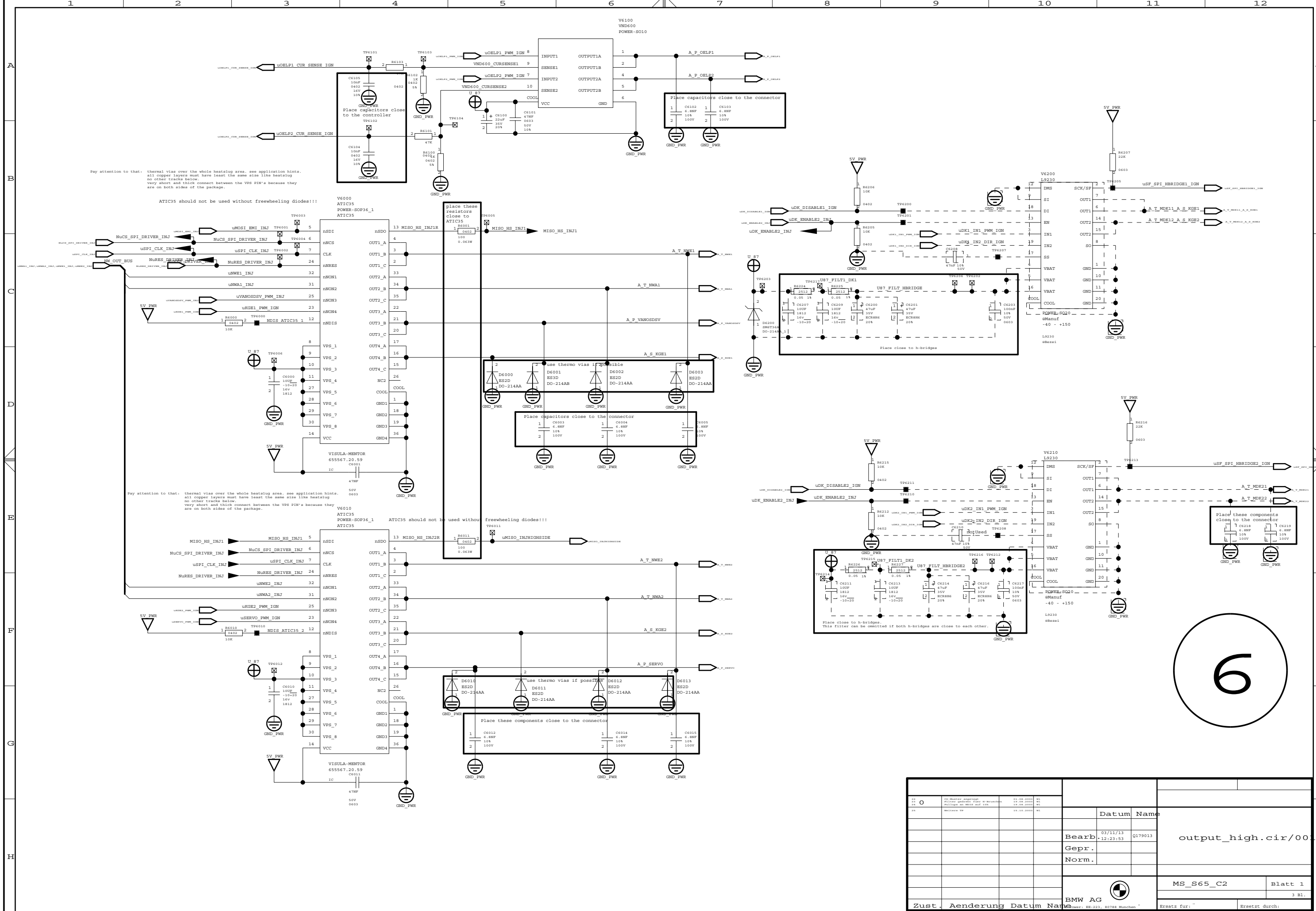
17	0	C2-Muster angelegt	01.08.2003				W1				
18		Pull-Up fuer BWS3	29.09.2003	W1		Datum	Name	interface.cir/001.s			
					Bearb.	03/11/13 12:23:43	Q179013				
					Gepr.						
					Norm.						
								MS S65_C2		Blatt 1	
										3 Bl.	
Zust.	Aenderung	Datum	Name	BMW AG Lower: RE-223, 80788 München "				Ersetzt fur: "		Ersetzt durch:	

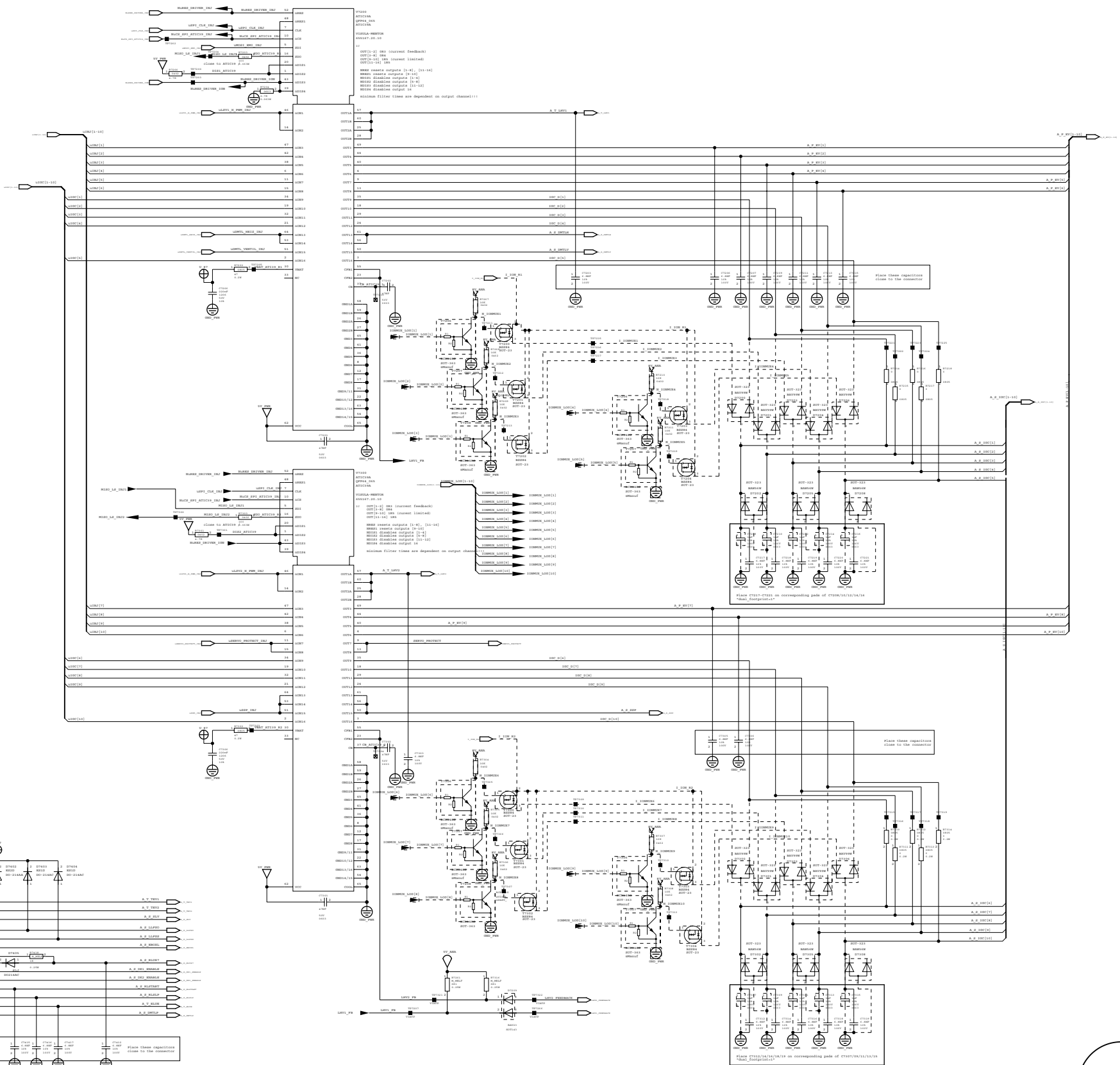
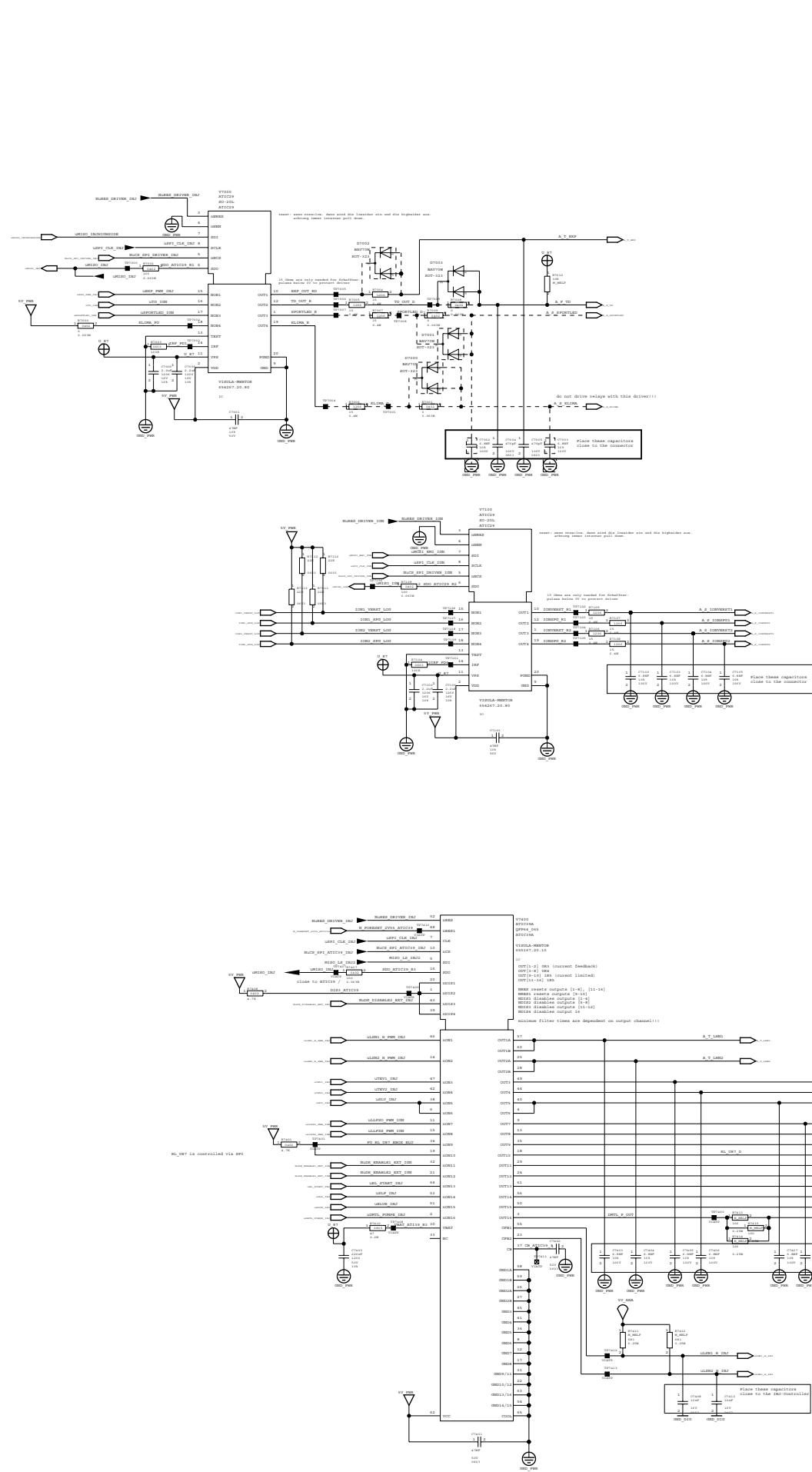


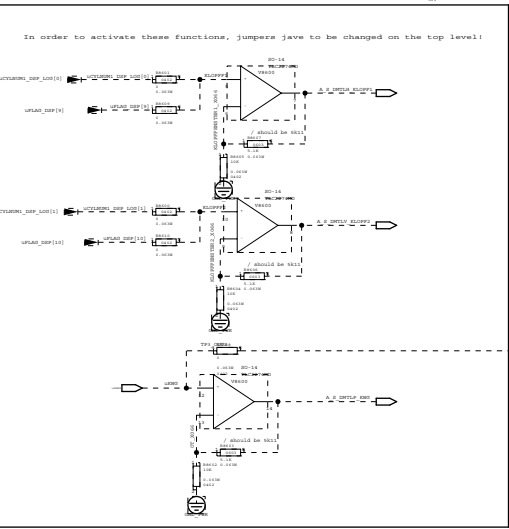
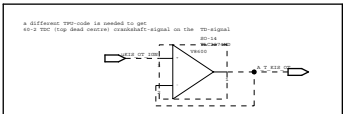
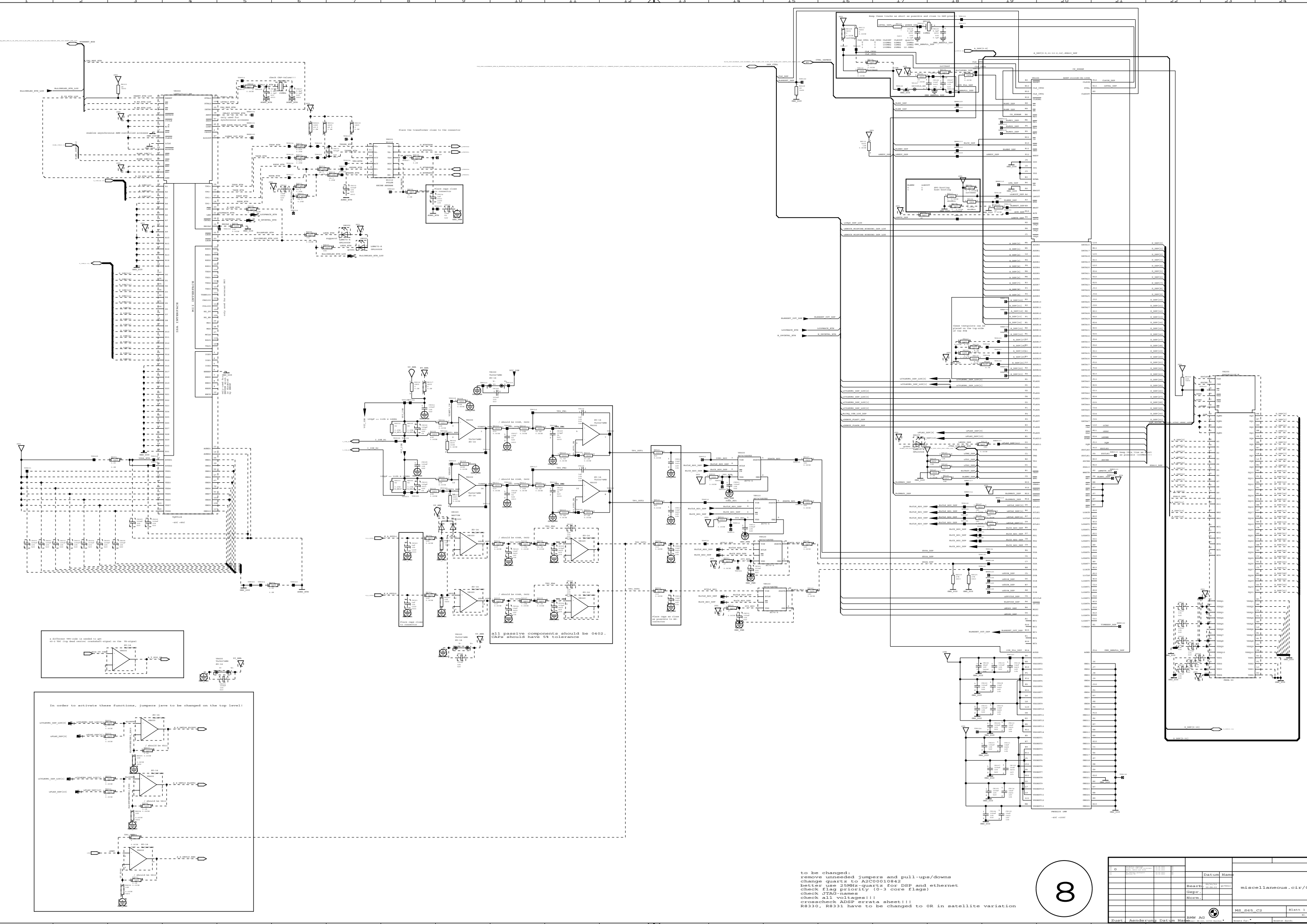


A3

Mit VISULA erstellt					Datum	Name	Kurzz.
11	C2-Muster erstellt	01.08.2003	Wi	Bearb.			
				Gepr.			
				Norm			
Zust.	Änderung	Datum	Name	Simul.			

[illegible]





to be changed:
remove unneeded jumpers and pull-ups/downs
change quartz to A2C00010842
better use 25MHz-quartz for DSP and ethernet
check flag priority (0-3 core flags)
check JTAG-names
check all voltages!!!
crosscheck ADSP errata sheet!!!
R8330, R8331 have to be changed to 0R in satellite variation

8

Datum Name		Date		Version	
Bearth		12.05.11		01001	
Gepf.					
Mowm.					
Zust. Aenderung Datum Name		BMW AG		MS_845_C2	
				Blatt 1	